8/16/05

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Claim 1 (Canceled).

OK to allow - Amy Awad 09/15/05

2. (Previously Presented) A method of driving a liquid crystal display device having a plurality of liquid crystal cells disposed in a matrix of rows and columns, comprising:

scanning the rows of liquid crystal cells in the liquid crystal display device sequentially; and

subsequently, resetting each liquid crystal cell of the liquid crystal display device simultaneously, wherein resetting each liquid crystal cell of the liquid crystal display device simultaneously comprises applying a reset voltage to a common electrode of the liquid crystal display device.

3. (Previously Presented) A method of driving a liquid crystal display device having a plurality of liquid crystal cells disposed in a matrix of rows and columns, comprising:

scanning the rows of liquid crystal cells in the liquid crystal display device sequentially; and

subsequently, resetting each liquid crystal cell of the liquid crystal display device simultaneously, wherein resetting each liquid crystal cell of the liquid crystal display device simultaneously comprises simultaneously applying a gate high voltage to a gate electrode line of each liquid crystal cell.

Claim 4 (Canceled).

Claim 5 (Canceled).

Claim 6 (Canceled).

Claim 7 (Canceled).

8. (Previously Presented) A method of resetting a liquid crystal display device, comprising applying a reset voltage to all liquid crystal cells of the liquid crystal display device to reset the liquid crystal display device, wherein the reset voltage is a gate high voltage simultaneously applied to gate electrode lines of the liquid crystal display device.

9. (Original) A reset circuit for a liquid crystal display device, comprising:

voltage selecting means for selecting, in response to an input control signal, a normal common voltage to be applied to a common electrode of the liquid crystal display device in an interval when a data voltage is charged and maintained in all liquid crystal cells of the liquid crystal display, and for selecting, in response to the input control signal, a reset voltage less than the normal common voltage to be applied to the common electrode in a reset interval.

10. (Original) A reset circuit for a liquid crystal display device, comprising:

a voltage amplifier for amplifying an input control signal having a specific logical state only in a reset interval when liquid crystal cells of the liquid crystal display device are reset, the

amplified input control signal to be applied to a common electrode of the liquid crystal display device.

11. (Original) The reset circuit as claimed in claim 10, wherein the voltage amplifier outputs a normal common electrode voltage in an interval when a data voltage is charged and maintained in the liquid crystal cells, and outputs a reset voltage less than the normal common electrode voltage in the reset interval.

12. (Original) A reset circuit for a liquid crystal display device, comprising:

a shift register for generating sequential gate driving signals;

logical OR gates for performing a logical OR operation of an input reset signal and each gate driving signal from the shift register; and

level shifters connected individually to outputs of the logical OR gates to select and output a gate voltage in accordance with a logical state of a signal outputted from each of the logical OR gates.

13. (Original) The reset circuit as claimed in claim 12, wherein each of the level shifters applies a gate high voltage to a corresponding gate line when an output signal of the corresponding logical OR gate is in a logical high state, and applies a gate low voltage to the corresponding gate line when an output signal of the corresponding logical OR gate is in a logical low state.

14. (Original) The reset circuit as claimed in claim 12, wherein the reset circuit is included in a gate driving integrated circuit.

Claim 15 (Canceled).

16. (Previously Presented) A liquid crystal display device, comprising:

a plurality of liquid crystal cells arranged in a matrix of rows and columns;

means for sequentially scanning the rows of liquid crystal cells;

means for simultaneously resetting all of the liquid crystal cells; and

a common electrode, wherein the means for simultaneously resetting all of the liquid crystal cells comprises means for applying a reset voltage level to the common electrode.

17. (Previously Presented) A liquid crystal display device, comprising:

a plurality of liquid crystal cells arranged in a matrix of rows and columns;

means for sequentially scanning the rows of liquid crystal cells;

means for simultaneously resetting all of the liquid crystal cells; and

further comprising a plurality of gate lines, each gate line being connected to a corresponding row of liquid crystal cells, wherein the means for simultaneously resetting all of the liquid crystal cells comprises means for simultaneously applying a gate high voltage to each gate line.